

# PC915

## Wide Band Linear Output Type OPIC Photocoupler

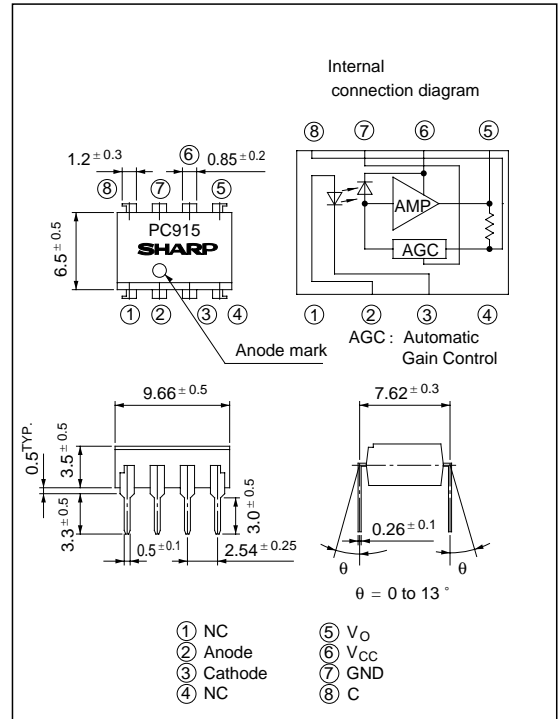
### ■ Features

1. Wide band linear output type  
(Frequency band width : TYP. 10Hz to 8MHz )
2. Fluctuation free stable output  
(Output fluctuation : TYP.  $\pm 5\%$  at within operating temperature 50 000hr )
3. High isolation voltage  
(  $V_{iso}$  : 5 000V<sub>rms</sub> )
4. Standard dual-in-line package
5. Recognized by UL, file No, E64380

### ■ Applications

1. Video signal insulation in TV
2. Insulation amplifier in measuring instrument and FA equipment

### ■ Outline Dimensions



\* "OPIC" ( Optical IC ) is a trademark of the SHARP Corporation.  
 An OPIC consists of a light-detecting element and signal-processing circuit integrated onto a single chip.

### ■ Absolute Maximum Ratings

(  $T_a = 25^\circ\text{C}$  )

Parameter		Symbol	Rating	Unit
Input	Forward current	$I_F$	25	mA
	Reverse voltage	$V_R$	6	V
	Power dissipation	P	45	mW
Output	Supply voltage	$V_{CC}$	- 0.5 to + 13	V
	Output power dissipation	$P_O$	250	mW
	Output current	$I_O$	- 1.0 to + 0.5	mA
*1 Isolation voltage		$V_{iso}$	5 000	V <sub>rms</sub>
Operating temperature		$T_{opr}$	- 25 to + 85	$^\circ\text{C}$
Storage temperature		$T_{stg}$	- 55 to + 125	$^\circ\text{C}$
*2 Soldering temperature		$T_{sol}$	260	$^\circ\text{C}$

\*1 40 to 60% RH, AC for 1 minute

\*2 For 10 seconds

## ■ Electro-optical Characteristics

(Unless otherwise specified, Ta = 25°C)

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit	Fig.	
Input	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10mA	-	1.6	1.8	V	1	
	Reverse voltage	I <sub>R</sub>	V <sub>R</sub> = 5V	-	-	10	μA	-	
	Terminal capacitance	C <sub>t</sub>	V = 0, f = 1MHz	-	60	250	pF	-	
Output	Supply current	I <sub>CC</sub>	I <sub>F</sub> = 10mA	-	9	16	mA	1	
	DC output voltage	V <sub>ODC</sub>	I <sub>F</sub> = 10mA	4	6	8	V	1	
	Output noise voltage	V <sub>ONO</sub>	I <sub>F</sub> = 10mA, Band width = 100Hz to 4.2MHz	-	4	-	mV <sub>rms</sub>	1	
Transfer characteristics	AC output voltage	V <sub>OAC</sub>	R <sub>E</sub> = 230Ω	0.8	1.0	1.2	V <sub>P-P</sub>	2	
	AC output voltage fluctuation	<sup>*1</sup> Temperature characteristics	ΔV <sub>OAC-1</sub>	R <sub>E</sub> = 230Ω, Ta = 10 to 70°C	-	± 3	-	%	2
		<sup>*2</sup> Forward current characteristics	ΔV <sub>OAC-2</sub>	R <sub>E</sub> = 230 to 460Ω	-	± 3	-	%	2
	<sup>*3</sup> Cut-off frequency	High frequency	f <sub>CH</sub>	R <sub>E</sub> = 230Ω	6	8	-	MHz	2
		Low frequency	f <sub>CL</sub>	R <sub>E</sub> = 230Ω	-	10	20	Hz	2
	Differential gain	DG		-	+ 3	-	%	3	
	Differential phase	DP		-	- 3	-	°	3	
	Isolation resistance	R <sub>ISO</sub>	DC500V, 40 to 60% RH	5 × 10 <sup>10</sup>	1 × 10 <sup>11</sup>	-	Ω	-	
Floating capacitance	C <sub>f</sub>	V = 0, f = 1MHz	-	0.6	5	pF	-		

\*1 Fluctuation ratio of V<sub>OAC</sub> at Ta = - 10 to 70°C on the basis of V<sub>OAC</sub> at Ta = 25°C

\*2 Fluctuation ratio of V<sub>OAC</sub> at R<sub>E</sub> = 230 to 460Ω on the basis of V<sub>OAC</sub> at R<sub>E</sub> = 230Ω

\*3 Frequency of V<sub>IN</sub> when V<sub>OAC</sub> falls by 3dB on the basis of V<sub>OAC</sub> when frequency of V<sub>IN</sub> in Fig. 2 is 100kHz.

## ■ Recommended Operating Conditions

Parameter		Symbol	MIN.	MAX.	Unit
Input	Forward bias current	I <sub>FB</sub>	8	15	mA
	Supply voltage	V <sub>CC</sub>	8	13	V
Output	AC output voltage	V <sub>OAC</sub>	-	4	V <sub>P-P</sub>
	Output current	I <sub>O</sub>	- 0.6	+ 0.2	mA
	C terminal capacitance	C <sub>C</sub>	10	-	μF

■ Test Circuit

Fig.1

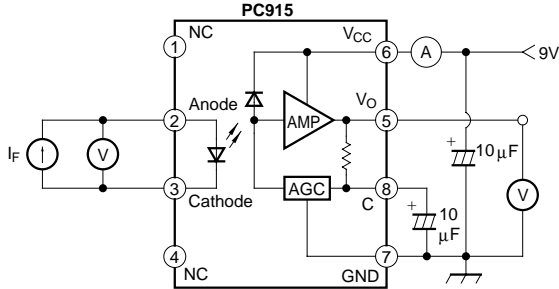
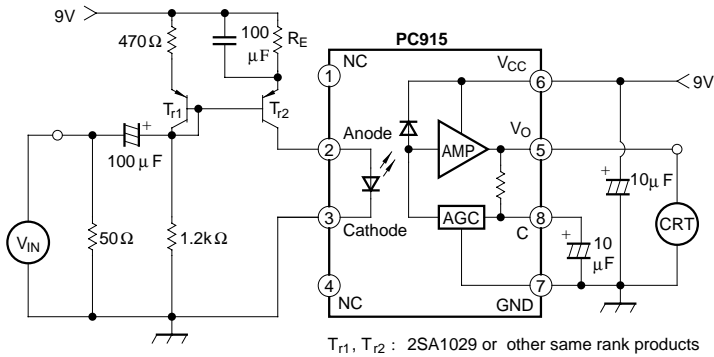


Fig. 2

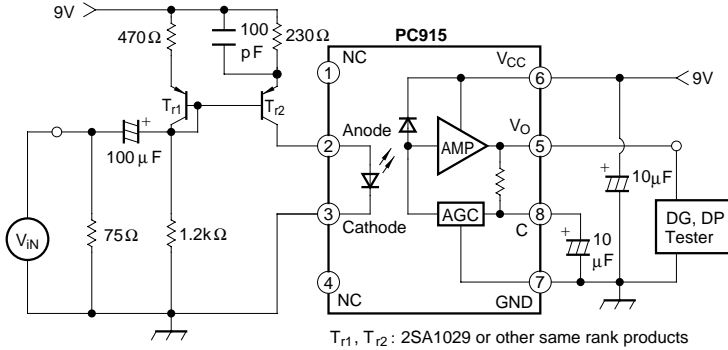


**V<sub>IN</sub> Waveform**

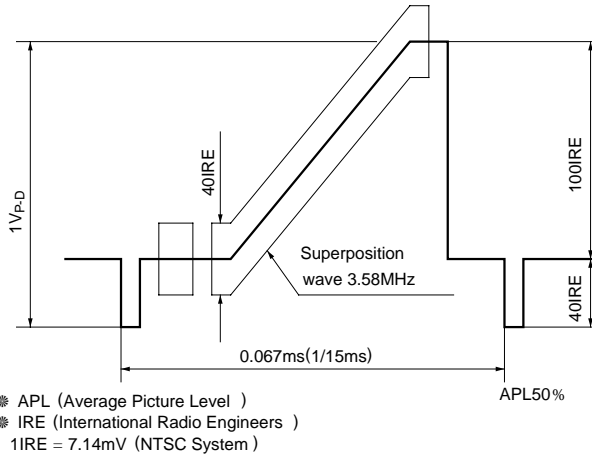


(Frequency) 15kHz at measuring  $V_{OAC}$ ,  $\Delta V_{OAC-1}$  and  $\Delta V_{OAC-2}$   
and shall be swept at measuring  $f_{CH}$  and  $f_{CL}$ .

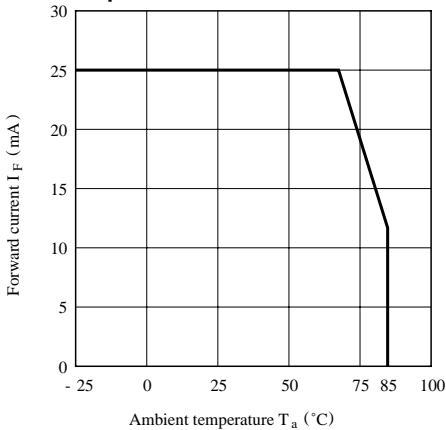
Fig. 3



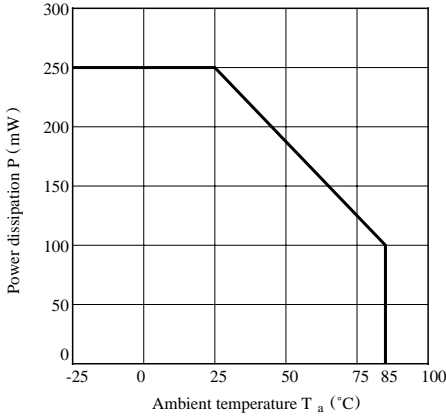
**V<sub>IN</sub> Waveform**



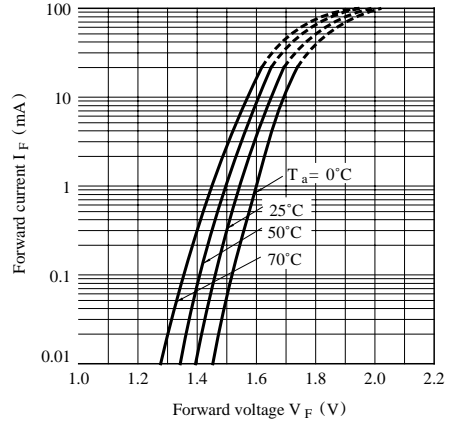
**Fig. 4 Forward Current vs. Ambient Temperature**



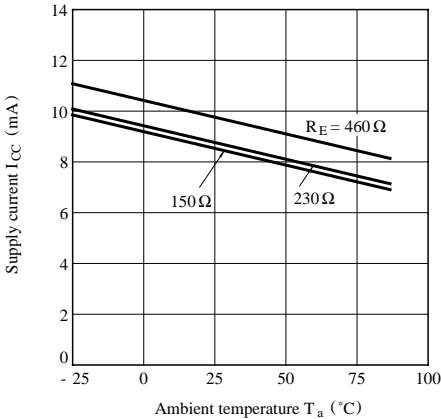
**Fig. 5 Power Dissipation vs. Ambient Temperature**



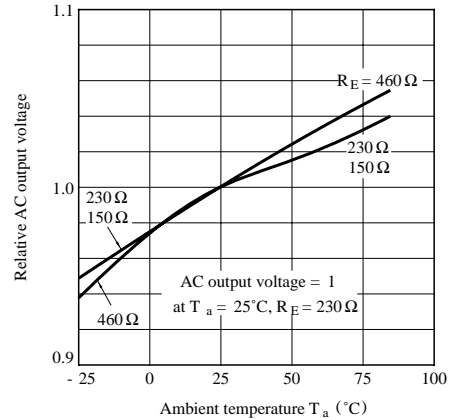
**Fig. 6 Forward Current vs. Forward Voltage**



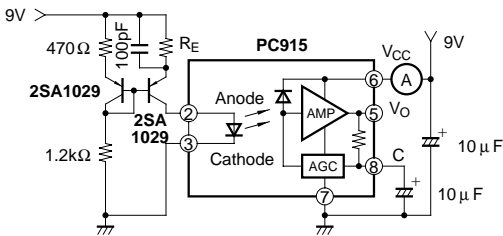
**Fig. 7 Supply Current vs. Ambient Temperature**



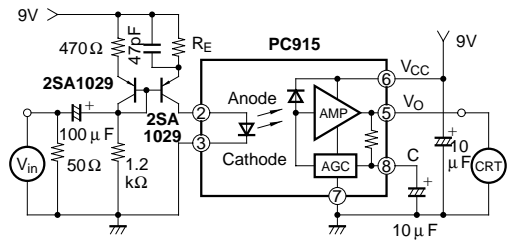
**Fig. 8-a Relative AC Output Voltage 1 vs. Ambient Temperature**



**Test Circuit of Supply Current**



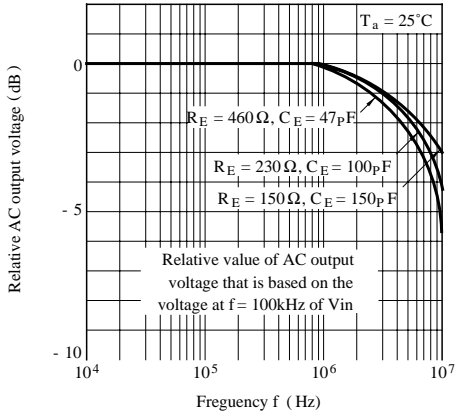
**Test Circuit of Relative AC Output Voltage 1 vs. Ambient Temperature**



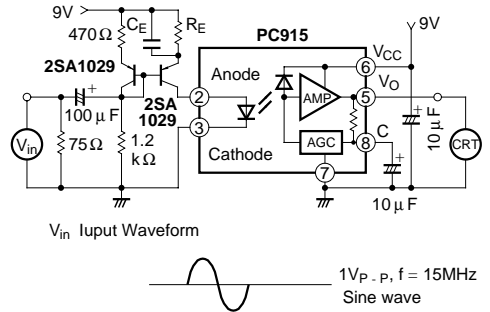
$V_{in}$  Input Waveform



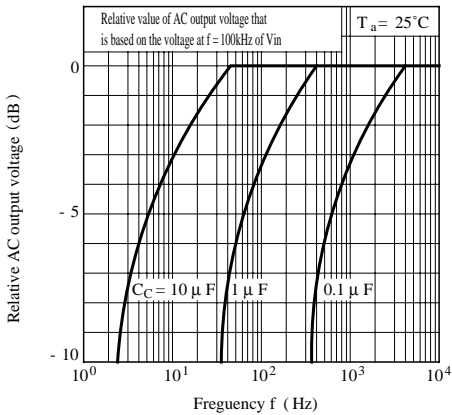
**Fig. 8-b Relative AC Output Voltage 2 vs. Frequency (1)**



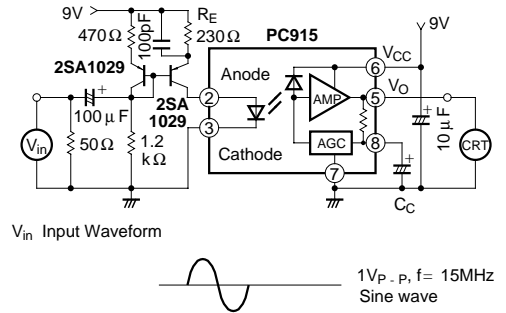
**Test Circuit of Relative AC Output Voltage 2 vs. Frequency (1)**



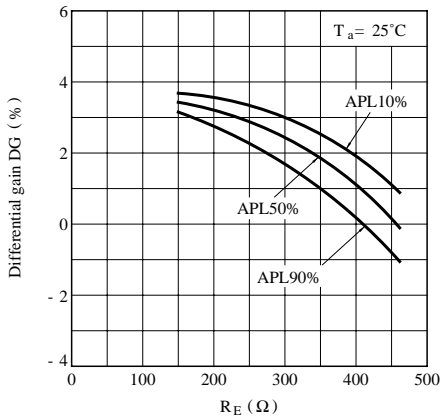
**Fig. 8-c Relative AC Output Voltage 2 vs. Frequency (2)**



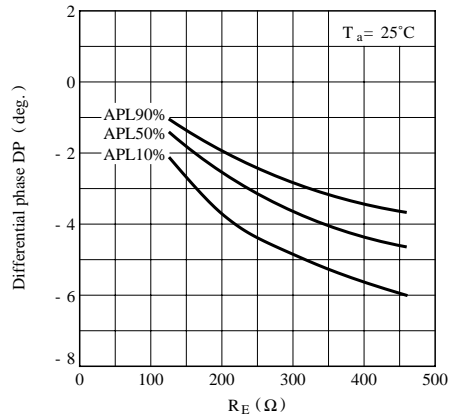
**Test Circuit of Relative AC Output Voltage 2 vs. Frequency (2)**



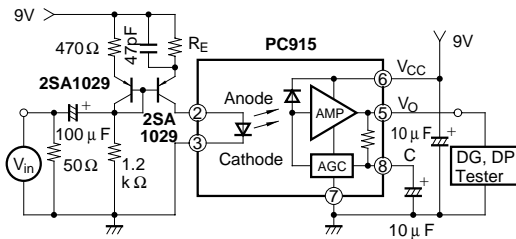
**Fig. 9 Differential Gain vs. RE**



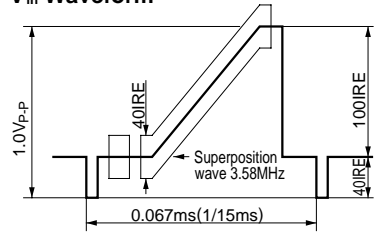
**Fig.10 Differential Phase vs. RE**



## Test Circuit of Differential Gain vs. $R_E$ and Differential Phase vs. $R_E$

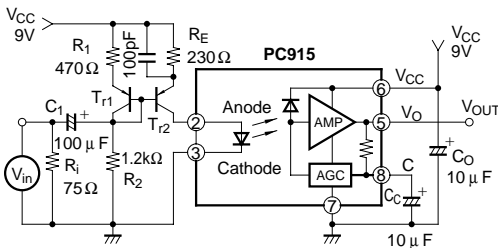


### $V_{in}$ Waveform



APL: Average Picture Level

## Application Example



$T_{r1}, T_{r2}$  : 2SA1029 or other same rank products

$$V_{OUT} = 2.3 \frac{I_s}{I_B} = 2.3 \frac{V_{in}}{V_{CC} - V_E}$$

$I_B$  : DC flowed to infrared LED

$I_s$  : AC flowed to infrared LED

$V_E$  : Emitter voltage of  $T_{r2}$  (Between emitter and GND)

## < Example of Circuit Setting >

### (1) Set for Gain

Gain is represented by the following formula ;

$$G = 2.3 / (V_{CC} - V_E)$$

When using on condition that Gain = 1, set  $V_{CC} - V_E$  on 2.3V. So that  $R_1$  and  $R_2$  is determined.

### (2) Set for Input Resistance

Set  $R_i$  on output impedance (usually  $75\Omega$ ) of a mounting equipment.

### (3) Set for $R_E$

When there is no signal (input signal : 0), set  $I_{LED}$  flowed into infrared LED on 10 mA.

### (4) Set for Low Cut-off Frequency

Low cut-off frequency with C terminal capacitance,  $C_c$ , is represented by the following formula;

$$f_c = 100 / C_c (\text{Hz}) (C_c : \mu\text{F value})$$

Then set  $C_i$  with input impedance of by-pass diode on as much value as possible on condition that  $f_c > 1 / (2\pi C_i R) [R = R_1 R_2 / (R_1 + R_2)]$

## Precautions for Use

- (1) It is recommended that a by-pass capacitor of more than  $0.01\mu\text{F}$  is added between  $V_{CC}$  and GND near the device in order to stabilize power supply line.
- (2) Handle this product the same as with other integrated circuits against static electricity.
- (3) As for other general cautions, refer to the chapter "Precautions for Use"

### NOTICE

- The circuit application examples in this publication are provided to explain representative applications of SHARP devices and are not intended to guarantee any circuit design or license any intellectual property rights. SHARP takes no responsibility for any problems related to any intellectual property right of a third party resulting from the use of SHARP's devices.
- Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device. SHARP reserves the right to make changes in the specifications, characteristics, data, materials, structure, and other contents described herein at any time without notice in order to improve design or reliability. Manufacturing locations are also subject to change without notice.
- Observe the following points when using any devices in this publication. SHARP takes no responsibility for damage caused by improper use of the devices which does not meet the conditions and absolute maximum ratings to be used specified in the relevant specification sheet nor meet the following conditions:
  - (i) The devices in this publication are designed for use in general electronic equipment designs such as:
    - Personal computers
    - Office automation equipment
    - Telecommunication equipment [terminal]
    - Test and measurement equipment
    - Industrial control
    - Audio visual equipment
    - Consumer electronics
  - (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection with equipment that requires higher reliability such as:
    - Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
    - Traffic signals
    - Gas leakage sensor breakers
    - Alarm equipment
    - Various safety devices, etc.
  - (iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:
    - Space applications
    - Telecommunication equipment [trunk lines]
    - Nuclear power control equipment
    - Medical and other life support equipment (e.g., scuba).
- Contact a SHARP representative in advance when intending to use SHARP devices for any "specific" applications other than those recommended by SHARP or when it is unclear which category mentioned above controls the intended use.
- If the SHARP devices listed in this publication fall within the scope of strategic products described in the Foreign Exchange and Foreign Trade Control Law of Japan, it is necessary to obtain approval to export such SHARP devices.
- This publication is the proprietary product of SHARP and is copyrighted, with all rights reserved. Under the copyright laws, no part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, for any purpose, in whole or in part, without the express written permission of SHARP. Express written permission is also required before any use of this publication may be made by a third party.
- Contact and consult with a SHARP representative if there are any questions about the contents of this publication.